

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Kiyoshi Kato	Art Unit :	2825
Serial No. :	10/595,567	Examiner :	Magid Y Dimyan
Filed :	April 27, 2006	Conf. No. :	2837
Title :	SEMICONDUCTOR INTEGRATED CIRCUIT AND DESIGN METHOD THEREOF		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF APRIL 16, 2008

Please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 7 of this paper.